

REMARKSObjections to the Drawings

The drawings have been amended with appropriate sheets attached hereto in order to overcome the objection.

Objections to the Specification

The specification has been amended to cure the informality.

Objections to the Claims

Applicant requests cancellation of claims 2, 7, and 11.

REJECTIONS UNDER 35 U.S.C. 102(b) and 103(a)

Neither Saulsbury, nor Westberg, either alone or in combination, disclose a data cache located on a memory module controlled by a command sequencer and serializer unit in a memory controller where the memory controller and memory module are coupled together via a memory bus as claimed in amended claim 1. It should be noted that the command sequence and serializer unit and array of tag addresses are not located on the memory module, as indicated by the language of claim one describing a memory bus coupling the memory controller the memory module.

Claims 6 and 10 similarly include limitations where the array of tags are located in a memory controller and the data cache is located on a memory module.

For these reasons, claims 1, 3-6, 8-10, and 12-15 are distinguished over and patentable over the Saulsbury and Westberg references.

CONCLUSION:

In view of the foregoing, Applicant submits that claims 1, 3-6, 8-10, and 12-15 are distinguished over the cited art and are in condition for allowance. Allowance of claims 1, 3-6, 8-10, and 12-15 is respectfully requested.

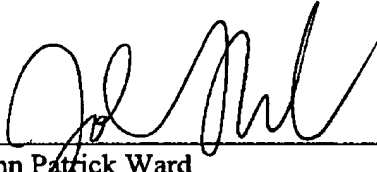
DEPOSIT ACCOUNT AUTHORIZATION

Authorization is hereby given to charge our Deposit Account No. 02-2666 for any additional charges that may be due.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

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